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NOIDA INSTITUTE OF ENGINEERING AND TECHNOLOGY, GREATER NOIDA

(An Autonomous Institute)

Affiliated to Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

M.Tech

FIRST YEAR (SEMESTER-II) THEORY EXAMINATION (2020-2021)

(Objective Type)

Subject Code: AMTVL0218

Subject: Real Time Operating System

Max. Mks. : 40

Time : 70 Minutes

General Instructions:

All questions are compulsory.

Question No- 1 to 5 are objective type question carrying 2 marks each.

Question No- 6 to 20 are also objective type/Glossary based question carrying 2 marks each.

Q.No	Question Content	Question Image	Category	Sub Category	Marks	Options Randomization	Type	Difficulty	Correct	Option1	Option2	Option3	Option4
1	Hard real time operating system has _____ jitter than a soft real time operating system.		Single Choice Questions	Single Choice Questions	2		Single Choice	Brilliant	less	less	more	equal	none of the mentioned
2	Where are the device drivers located in RTOSs with a microkernel.		Single Choice Questions	Single Choice Questions	2		Single Choice	Brilliant	In the user space	In the kernel space	In the user space	In separately allocated space which is neither kernel space nor user space.	None
3	Which of the following are header files?		Single Choice Questions	Single Choice Questions	2		Single Choice	Brilliant	#include	#include	File	struct()	proc()
4	The total number of elements (empty or not) in the queue is the _____ of the queue.		Single Choice Questions	Single Choice Questions	2		Single Choice	Smart	total length	total length	total range	space	None
5	How many maximum standard tasks can be active in system.		Single Choice Questions	Single Choice Questions	2		Single Choice	Brilliant	16	15	16	14	13
6	The ability of the Real time operating system to provide a required level of service in a_____response time		Glossary I	Glossary I	2		Single Choice	Brilliant	Bounded	Soft	Bounded	Deadlines	
7	Task scheduling is the primary mechanism for making applications meet their respective _____.		Glossary I	Glossary I	2		Single Choice	Brilliant	Deadlines	Soft	Bounded	Deadlines	
8	Multimedia transmission and reception are the example of _____RTOS.		Glossary I	Glossary I	2		Single Choice	Smart	Soft	Soft	Bounded	Deadlines	
9	The hardware mechanism that allows a device to notify the CPU is called _____.		Glossary II	Glossary II	2		Single Choice	Brilliant	Interrupt	Demand paging	Process Identifier	Interrupt	
10	A process is copied into the main memory from the secondary memory is called as_____.		Glossary II	Glossary II	2		Single Choice	Genius	Demand paging	Demand paging	Process Identifier	Interrupt	
11	In UNIX, each process is identified by its _____.		Glossary II	Glossary II	2		Single Choice	Brilliant	Process Identifier	Demand paging	Process Identifier	Interrupt	
12	Assembling of each source file generates a corresponding _____ file.		Glossary III	Glossary III	2		Single Choice	Brilliant	Object	Hex	Machine	Object	
13	Absolute object file converted into _____file then burn into microcontroller.		Glossary III	Glossary III	2		Single Choice	Brilliant	Hex	Hex	Machine	Object	
14	Cross-compiler’ converts the high level language to target processor specific _____ code.		Glossary III	Glossary III	2		Single Choice	Brilliant	Machine	Hex	Machine	Object	

Q.No	Question Content	Question Image	Category	Sub Category	Marks	Options Randomization	Type	Difficulty	Correct	Option1	Option2	Option3	Option4
15	Deleting a message queue automatically _____ waiting tasks.		Glossary IV	Glossary IV	2		Single Choice	Genius	Unblocks	Data	Lost	Unblocks	
16	Messages that were queued are_____ when the queue is deleted.		Glossary IV	Glossary IV	2		Single Choice	Brilliant	Lost	Data	Lost	Unblocks	
17	_____ storage and Access by Multiple Tasks are the characteristics of queue.		Glossary IV	Glossary IV	2		Single Choice	Smart	Data	Data	Lost	Unblocks	
18	Win3.X is an example of _____ scheduling algorithm.		Glossary V	Glossary V	2		Single Choice	Brilliant	Round robin	Preemptive	Round robin	Context switch	
19	Win95 is an example of _____ scheduling algorithm.		Glossary V	Glossary V	2		Single Choice	Brilliant	Preemptive	Preemptive	Round robin	Context switch	
20	RTX will use the_____ time within the Cortex-M processor.		Glossary V	Glossary V	2		Single Choice	Smart	Context switch	Preemptive	Round robin	Context switch	